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Microprocessor Architecture
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Interfacing (140701) Rahul Patel 2
Points to be Discussed • 8085
Microprocessor

Chapter 4 8085 Microprocessor
Architecture and Memory ...

Microprocessor - 8085 Architecture -
8085 is pronounced as eighty-eighty-five

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microprocessor. It is an 8-bit microprocessor designed by Intel in 1977 using NMOS technology.

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Microprocessor & Interfacing (140701)

Rahul Patel 1 Chapter 4 8085

Microprocessor Architecture and
Memory Interfacing by Rahul Patel,
Assistant Professor, EC Dept., Chapter
3 The 8085 Microprocessor
Architecture. Intel 8085 CPU Block
Diagram. The 8085 and Its Buses. The
8085 is an 8-bit general purpose
microprocessor that can address 64K
Byte ...

PDF 8085 Microprocessor Architecture
And Instruction Set ...

CHAPTER-1 Basics of Microprocessor

DTEL . 1 Topic 1: 2 Topic 2: 3 Topic 3:

4 Topic 4: 5 5 Topic 5: Evolution of

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Microprocessor and types 8085
Microprocessor, Salient features of 8085
Architecture of 8085 -Functional Block
diagram, Pin description,

Microprocessor and Programming
Tutorial On Introduction to 8085
Architecture and Programming .

Contents 1. Internal architecture of
8085 microprocessor 2. 8085 system bus
3. 8085 pin description. 4. 8085
functional description. 5. Programming
model of 8085 microprocessor ... in the
chapter "Stack and Subroutines."

Tutorial On Introduction to 8085
Architecture and Programming

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Microprocessors. Learn vocabulary,
terms, and more with flashcards, games,
and other study tools. ... dual-core
architecture. ... CPU feature that

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enables the microprocessor to support running multiple operating systems at the same time.

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An instruction is a command to the microprocessor to perform a given task on a specified data.; An instruction manipulates the data and a sequence of instructions constitute of program.

Generally instruction has two components namely operation code (Opcode) field and address (operand) field.

Chapter 2: Intel 8085 Microprocessor Architecture and ...

The architecture of 8085 microprocessor provides the idea about what are the operations to be executed and how these are performed. It can

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perform operations that are given below
: Operates on and stores 8-bit data.

What is 8085 Microprocessor?

Definition, Architecture ...

1. Chapter 1 – Introduction 2. Chapter
2 – Programming with 8085
Microprocessor 3. Chapter 3 –
Programming with 8086
Microprocessor 4. Chapter 4 –
Microprocessor System 5. Chapter 5-
Interrupt Operations 6. Chapter 6 –
Advanced Topics. Note Pulchowk Note
Old Question Collection Important
Question Pin Diagram and Pin
Description of 8086

Microprocessor - BCT Engineering
Notes

Chapter 5 The 8085 Microprocessor 5.1
Architecture of 8085 Microprocessor
5.1.1 Register Section 5.1.2 Address

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Buffer and Address-Data Buffer 5.1.3
Arithmetic and Logical Unit (ALU)
5.1.4 Timing and Control Unit 5.1.5
Interrupt Control 5.2 Pin Description of
8085 5.3 Instruction Set of 8085
Microprocessor 5.4 Timing Diagram for
8085 Instructions

AN INTRODUCTION T M 8085 -
EduTechLearners

Microprocessor 8085 Chapter 3 1. 1
Chapter 3 Microprocessor Architecture
2. Microprocessor Architecture 2 The
microprocessor can be programmed to
perform functions on given data by
writing specific instructions into its
memory. The microprocessor reads one
instruction at a time, matches it with its
instruction set, and performs the data
manipulation specified. The result is
either stored back ...

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A Microprocessor is a multipurpose, Programmable clock-driven, register based electronic device that read binary instruction from a storage device called memory, accepts binary data as input and processes data according to those instructions and provides results as outputs.

Microprocessor: Chapter 1:
Introduction to Microprocessor
lec 3 - Architecture and Organization of
8085 (Cont.) ... 8086 Microprocessor
Architecture Tutorial With Working
Mechanism Explained Part-2 ...
Architecture and Organization of 8085 -
Duration: ...

lec 3 - Architecture and Organization of
8085 (Cont.)

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Link: Chapter 4 Notes. Unit 5. Link:
Chapter 5 Notes. ... UNIT-I. An over
view of 8085, Architecture of 8086
Microprocessor. Special functions of
General purpose registers. 8086 flag
register and function of 8086 Flags.
Addressing modes of 8086. Instruction
set of 8086. Assembler directives, simple
programs, procedures, and macros. ...

Microprocessor and Interfacing Pdf
Notes - MPI Notes Pdf

Chapter 3 Microprocessor Architecture
and Microcomputer Systems by Rahul
Patel, Assistant Professor, EC Dept.,
Sankalchand Patel College of Engg.,
Visnagar. ... microprocessor. – Hold

- The 8085 has a pin called HOLD. This
pin is used by external devices to gain
control of the busses.

Chapter 3 Microprocessor Architecture

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and Microcomputer ...

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Intel 8086 Microprocessor 40 8086

Architecture 46 Minimum Mode

Operation of 8086 57 8086 Addressing

Modes 68 8086 Microprocessor

Instruction 92 Assembler Directives 109

I-O Interfacing 127

Note for Microprocessor - MP By
Annapurna Mishra

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